By developing small, lithographically-fabricated devices based on Josephson junctions, we hope to achieve an unprecedented level of stability and reliability over previous technologies based on gas lasers and whisker-contacted diodes. Lithography also enables the integration of multiple devices into circuits in order to perform more complex frequency translation functions. LTS fabrication technology is well-developed. However, HTS multilayer thin-film fabrication is still under development.

Many different devices can be implemented using Josephson technology, such as mixers, oscillators, detectors, and logic elements. This correspondence will focus on Josephson oscillators and their application to the submillimeter (> 300 GHz) and THz frequency regimes. We present experimental results from LTS devices that were designed to demonstrate the fundamental principles of Josephson oscillators at accessible microwave frequencies.

II. PRINCIPLES OF JOSEPHSON OSCILLATORS

We use the zero-capacitance, resistively-shunted junction (RSJ) model [3], [4] to compare Josephson oscillators based on single junctions and arrays of junctions. Our discussion will be limited to nonresonant junctions, so no comments will be made on flux-flow oscillators [5]. Furthermore, we assume lumped arrays having physical dimensions smaller than 1/8 of the emission wavelength. Finally, we assume that ground planes are used with all circuits, preventing self-field coupling between devices. Within this model, we discuss specific design criteria required to achieve impedance matching, high output power, and narrow linewidth at a given operation frequency.

A. Single Junctions

A RSJ is characterized by a critical current $I_c$ and resistance $R$ and is biased with a dc current source $I$. The equations describing a RSJ are

\[ I = \frac{\Phi_0}{2\pi R} \cdot \sin \phi \]

\[ V = \frac{\Phi_0}{2\pi} \cdot \phi \]

where $\phi(t)$ is the macroscopic phase difference across the junction, $\Phi_0 = h/2e \approx 2.07 \text{ mVTHz}$ is the magnetic flux quantum, $V$ is the instantaneous voltage across the junction, and the overdot denotes differentiation with respect to time.

Equation (1) can be solved analytically. Fig. 1(a) shows the dc $I$-$V$ curve of a RSJ. For $I \leq I_c$, the junction is in the zero-voltage state, while for $I > I_c$, a voltage develops across the junction. In the voltage state, the supercurrent $I_c \sin \phi$ of a junction oscillates at a well-defined fundamental frequency $f = \Phi_0 / \Phi_0$. This is known as the Josephson effect, where the junction supercurrent oscillates at $1/f \approx 483 \text{ GHz}$ per mV of bias voltage, means that the junction is intrinsically a high-frequency dc-to-ac converter and can be used as a voltage-controlled oscillator.

Fig. 1(b) shows the amplitude of the fundamental Josephson oscillation and the first two higher harmonics as a function of voltage. When the junction is biased at voltages greater than the characteristic voltage $V_c = I_c R$, the time-dependent voltage is nearly sinusoidal because the amplitudes of the higher harmonics $V_{n} (n \geq 2)$ are much smaller than the amplitude of the fundamental. The peak voltage amplitude at the fundamental frequency approaches a maximum $V_1 \approx V_c$ for $V_{dc} \geq V_c$, and the junction impedance becomes...
resistive, $Z \approx R$. In order to maximize the fundamental power and minimize the contribution from harmonics, the characteristic frequency, $f_c = V_c/\Phi_0$ should be fixed at the minimum operating frequency, and the device should be operated at $V_{dc} \geq V_c$. Under these conditions, the rms available power from the junction is

$$P_{\text{dJ}} = \frac{V_c^2}{8R} = \frac{I_c V_c}{8}.$$  \hspace{1cm} (2)

If we assume that all Josephson junction technologies have a maximum critical current, then the available power is limited by the maximum $I_c$ and the minimum operating frequency. Typically it is assumed that $I_c$ is limited to a few milliamperes by nonuniform current flow due to self-induced magnetic fields [6]. The characteristic voltage is limited for a given technology by the gap frequency of the superconducting material. Typical Nb junctions have a maximum characteristic voltage $V_c \approx 2.8$ mV. Under these optimal conditions, the available power from a single junction could approach $P_{\text{dJ}} \approx 1 \mu$W for operation above 1.4 THz. Lower minimum operating frequencies will yield lower power.

If the junction is coupled to a resistive load $R_L$ and $V_{dc} \geq V_c$, the most efficient power transfer occurs when the junction and load resistances are matched ($R = R_L$). The rms power delivered to this matched load is then equal to the available power. Demanding $R_L = 50 \Omega$ load and $f_c = 1.4$ THz limits the critical current to only $I_c = V_c/R_L = 58 \mu$A. The power delivered to a matched 50 $\Omega$ load for this example is only 20 nW. In the highest frequency demonstration of a single junction Josephson oscillator, a tunnel junction delivered 10 nW at 1 THz to another junction on the same chip [7].

### B. Phase-Locked Arrays

Arrays of junctions have better impedance matching, higher power, and narrower linewidth than single junctions. Fig. 2 shows the geometry of a 2-D array. The array impedance depends on its width and height, $M$ columns each with $N$ junctions in the voltage state. The available power from a phase-locked 2-D array in the sinusoidal operating regime ($V_{dc} \geq V_c$ for each junction) is proportional to the number of phase-locked junctions, $P_{\text{d}} = \frac{MNLV_c}{8}$. The length-to-width ratio can be chosen to achieve impedance matching $N/M = R_L/R$, so that the power delivered to a matched load is

$$P_{\text{d}} = \frac{NMILV_c}{8} = \frac{M^2I_c^2R_L}{8}.$$  \hspace{1cm} (4)

With $V_c$ fixed and $I_c$ maximized (2.8 mA for Nb junctions), the total power delivered to a matched load is proportional to the square of the number of columns $M$. This gives 2-D arrays an additional degree of freedom over 1D arrays to increase and tune the power delivered to a fixed load. For the 1.4 THz minimum operating frequency ($V_c = 2.8$ mV), the power can be tuned to 4.9 mW by choosing $M = 10$ and $N = 500$ to match the array to a 50 $\Omega$ load. This delivered power is sufficient for both on-chip or off-chip applications, and the 5000-junction integration is achievable with present fabrication technology. Even higher power levels can be achieved by increasing the number of junctions accordingly.

For phase-locked arrays of junctions, the linewidth is reduced by the total number of phase-locked junctions [6], [13]

$$\Delta f_{\text{d}} = \frac{\Delta f_{\text{J}}}{MN}.$$  \hspace{1cm} (5)
Arrays with $M = 10$ and $N = 500$ would have a linewidth of $\sim 33$ kHz at 4 K, which is sufficiently narrow for most applications.

III. EXPERIMENTAL RESULTS

The first demonstration of complete phase locking in 2-D arrays was accomplished by using single Josephson junctions as on-chip detectors [12], [13]. Emission was coupled from a 2-D array to a detector junction through a dc-blocking capacitor. The capacitor enabled the array and the detector junction to be biased independently.

When the array was biased in the voltage state, emission was coupled to lock to the array’s emission. This phenomenon is observed in the detector’s I-V curve as flat voltage steps at voltages $V_{n0} = n\Phi_0 f$, where $f$ is the drive frequency and $n = 0, 1, 2, \ldots$ is the step number. These steps are called Shapiro steps. The number of steps and their widths depend on the amplitude of the drive.

Fig. 3 shows the detector junction I-V curve for a 10-by-10 junction 2-D array coherently emitting at 150 GHz [12], [13]. Since the 0.4-μW coupled to the detector junction at this frequency agrees with the expected theoretical power, all 100 junctions in the array are phase locked. This array showed Shapiro steps at voltage tunable frequencies ranging from 60–210 GHz, indicating broad band tunability.

More recently [15], the emission from 10 × 10 arrays has been coupled off-chip to room-temperature mixers through a fin-line antenna and WR-12 waveguide. A single voltage-tunable peak was detected in the frequency range from the 53-GHz cutoff frequency of the waveguide up to 230 GHz. Fig. 4 shows an example spectral peak with a linewidth of $\sim 13$ kHz.

These results were the first off-chip linewidth and power measurements of phase-locked 2-D arrays where a detailed comparison was made with theory. It was found that a stripline resonance in the antenna reduced the array dynamic resistance and thereby the emission linewidth (see 3)) from the $\sim 2$-MHz linewidth expected for a nonresonant-coupled array. From 84–90 GHz, the linewidth varied approximately one order of magnitude.

IV. CONCLUSION

Josephson oscillators are voltage tunable with potential emission at GHz and THz frequencies. LTS 2-D arrays have demonstrated complete phase locking and narrow linewidths over broadly-tunable microwave frequencies. HTS technology is desirable for Josephson oscillators because the higher intrinsic frequencies would enable higher-frequency oscillators. The impact of HTS Josephson devices on precision frequency synthesis and measurement depends largely upon improvement of HTS thin-film fabrication technology.

REFERENCES


